

IN THE CLAIMS

Please amend the claims as follows:

Claims 1-6 (Canceled).

Claim 7 (Original): An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that:

said real time processing unit has a cumulative addition processing function of, when each pixel data photographed by said image pickup device and inputted sequentially extends multiple frames, repeating, a predetermined number of times, a cumulative addition processing~in which a pixel data residing on the same position in the preceding frame temporarily stored in said main memory is added to each pixel data in each of said frames from said image pickup device and the result is stored in said memory.

Claim 8 (Original): An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that:

said real time processing unit has a circulating addition processing function of, when each pixel data photographed by said image pickup device and inputted sequentially extends multiple frames, repeating, a predetermined number of times, a circulating addition processing in which a pixel data residing on the same position in the preceding frame temporarily stored in said main memory and each pixel data in each of said frames from said image pickup device are respectively subjected to multiplication with a predetermined weighting factor, followed by addition, and the results are stored in said memory; and said weighting factor used in said circulating addition processing comprising a first factor to be multiplied to a pixel data residing at the same position in the preceding frame temporarily stored in said main memory, and a second factor to be multiplied to each pixel data in each frame from said image pickup device, said first and second factors being set such that the sum of these factors is always one.

Claim 9 (Original): An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that:

said real time processing unit has a pixel compensation function with which each pixel data photographed by said image pickup device and inputted sequentially is multiplied by a predetermined pixel compensation parameter previously stored in said main memory, for a predetermined pixel compensation including shading compensation.

Claim 10 (Original): An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that: said real time processing unit has a function of selecting at least said cumulative addition processing function as defined in claim 7, and said circulating addition processing function as defined in claim 8.

Claim 11 (Original): An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that:

said real time processing unit has a function of selecting said cumulative addition processing function as defined in claim 7, said circulating addition processing function as defined in claim 8, and said pixel compensation function as defined in claim 9.

Claims 12-27 (Canceled).